

CURRICULUM VITAE	
Mr. Abidali G Surati M.E (Electronics & Telecommunication) B.E. (Electronics)	ADDRESS :- A/P- Plot-15, Khoja colony, Sahayadri nagar, Sangli. Pin-416416.
Email ID:- agsurati@gmail.com	Mob. No: - 7249383240

❖ OBJECTIVE:
To obtain a suitable position at an established organization where I can utilize my technical knowledge skillfully & efficiently to achieve organizational goal through commitment, hard work, innovation, team work and grow along with organization by learning new things in technical fields.

❖ POST GRADUATION ENGINEERING QUALIFICATION:				
INSTITUTE: - Annasaheb Dange College of Engineering & Technology, Ashta.				
EXAM	YEAR OF PASSING	UNIVERSITY	% MARKS	CLASS
M.E (E & Tc)	2014 – 2015	Shivaji University, Kolhapur	63.89 %	FIRST CLASS

❖ ENGINEERING QUALIFICATION:				
INSTITUTE: - Padmabhushan Vasantdada Patil Institute & Technology, Budhgaon.				
EXAM	YEAR OF PASSING	UNIVERSITY	% MARKS	CLASS
B.E (Electronics)	2006 – 2007	Shivaji University, Kolhapur	67.06 %	DISTINCTION

❖ PRE-ENGINEERING QUALIFICATION:				
EXAM	YEAR OF PASSING	BOARD	% MARKS	CLASS
Diploma (E & C)	2002 – 2003	MSBTE	62.19 %	FIRST CLASS
S.S.C.	1999 – 2000	Kolhapur Board	67.20 %	FIRST CLASS

❖ TECHNICAL SKILLS:	
PROGRAMMING LANGUAGES	EMBEDDED C, ASSEMBLY.
OPERATING SYSTEMS	DOS. WINDOWS-XP/7/8
TOOLS	MATLAB, MV-KEIL, XILINX-10, MULTISIM, VB 6, NS-2

❖ TEACHING EXPERIENCE:			
INSTITUTE	DESIGNATION	WORKING DURATION	EXPERIENCE
P.V.P.I.T. Budhgaon.	Assistant Professor	14/10/2015 - 31/05/2016	1 year
MBTC, Islampur.	Assistant Professor	26/07/2014 - 30/04/2015	1 year
A.D.C.E.T. Ashta.	Assistant Professor	23/08/2010 - 09/05/2014	4 Years

❖ INDUSTRIAL EXPERIENCE:			
NAME OF INDUSTRY	DESIGNATION	WORKING DURATION	EXPERIENCE
Quality power Electrical Equipment Pvt, Ltd. Kupwad.	Testing Engineer	02/08/2007 to 30/4/2010	3 years
Padmini Industries, Sangli.	Service Engineer	12/04/2004 to 20/05/2005	1 year

❖ PAPER PUBLICATIONS /CONFERENCES:	
1	“An overview to cryptography And its aspects” in International journal IJERT Volume 2, Sept. 2013.
2	“Design & Simulation of FPGA based Multilevel PWM Single Phase Inverter Using VLSI Software” in ICIKR-ETS 2012 at Rajkot from 24 th to 25 th Mar 2012.
3	“ Review on Load sharing & Energy saving techniques used to increase the lifetime of Wireless sensor network” in IJATES, Volume- 02, Issue 07, July 2014.
4	“Parallel Load sharing technique used to increase lifetime of wireless sensor network” in IJRITCC, Volume- 03, Issue 03, March 2015.

❖ FACULTY DEVELOPMENT / STTP PROGRAMS ATTENDED:			
Sr. No	Title of the Program	Venue and Dates	Duration
1	Signal Processing Applications with MATLAB	ADCET, Ashta, 27 th to 31 st December 2010.	1 week
2	Research Methodology in Engineering & Applied Sciences	ADCET, Ashta , 25 th to 29 th April 2012	1 week
3	High Speed Digital System Design	ADCET, Ashta, 22 nd & 23 rd March 2013	2 days
4	Advances in Wireless Communication.	ADCET, Ashta , 24 th to 28 th June 2013	1 week
5	Soft skills & Motivational Techniques in Teaching	PVPIT, Budhgaon, 20 th to 24 th June 2016.	1 week

❖ EXAMINATION DUTIES:	
1. Worked as “ External Senior Supervisor ” for SRPD (F.E. online Question paper) SUK Examination May 2015 for Semester I during 06/05/2015 to 27/05/2015 at ADCET, Ashta.	
2. Worked as External oral examiner for the subject Digital electronics at S.E (E & Tc) from 19/11/2014 to 21/11/2014 at NMCOE, Peth naka.	

❖ PERSONAL DETAILS:	
NAME	SURATI ABIDALI GULAMABBAS.
D.O.B	18 th May 1983.
GENDER	MALE
MARITAL STATUS	MARRIED
NATIONALITY	INDIAN
LANGUAGES KNOWN	ENGLISH, HINDI, MARATHI, GUJARATI.

❖ DECLARATION:
I HEREBY DECLARE THAT THE ABOVE WRITTEN PARTICULARS ARE TRUE TO THE BEST OF MY KNOWLEDGE AND BELIEF.

(SURATI ABIDALI GULAMABBAS)